



Low Voltage SOT-23 Temperature Sensors

ADT45/ADT50

FEATURES

- Low Voltage Operation (2.7 V to 12 V)
- Calibrated Directly in °C
- 10 mV/°C Scale Factor
- ±2°C Accuracy Over Temperature (typ)
- ±0.5°C Linearity (typ)
- Stable with Large Capacitive Loads
- Specified -40°C to +125°C, Operation to +150°C
- Less than 60 mA Quiescent Current
- Low Self-Heating

APPLICATIONS

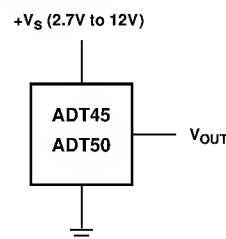
- Environmental Control Systems
- Thermal Protection
- Industrial Process Control
- Fire Alarms
- Power System Monitors
- CPU Thermal Management

GENERAL DESCRIPTION

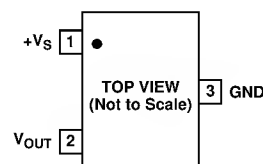
The ADT 45 and ADT 50 are low voltage, precision centigrade temperature sensors. They provide a voltage output that is linearly proportional to the Celsius (Centigrade) temperature. The ADT 45/ADT 50 do not require any external calibration to provide typical accuracies of ±1°C at +25°C and ±2°C over the -40°C to +125°C temperature range. The low output impedance of the ADT 45/ADT 50, linear output and precise calibration simplify interfacing to temperature control circuitry and A/D converters. All three devices are intended for single supply operation from 2.7 V to 12 V maximum. Supply current runs well below 60 μA providing very low self-heating—less than 0.1°C in still air. The ADT 45/ADT 50 are functionally and pin compatible with LM 45/LM 50 respectively. The ADT 45 provides a 250 mV output at +25°C and reads temperature from 0°C to +100°C. The ADT 50 is specified from -40°C to +125°C, provides a 750 mV output at +25°C and operates to +125°C from a single 2.7 V supply. Both the ADT 45 and ADT 50 have an output scale factor of +10 mV/°C. Operation extends to +150°C with reduced accuracy for all devices when operating from a 12 V supply.

The ADT 45/ADT 50 are available in the low cost 3-lead SOT-23 surface mount package.

FUNCTIONAL BLOCK DIAGRAM



PACKAGE TYPES AVAILABLE SOT-23



REV. 0

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Analog Devices.

One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106, U.S.A.
Tel: 781/329-4700 World Wide Web Site: <http://www.analog.com>
Fax: 781/326-8703 © Analog Devices, Inc., 1997

ADT45/ADT50- SPECIFICATIONS ($V_S = +2.7\text{ V to }+5.5\text{ V}$, $-40^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
ACCURACY						
ADT 45		$T_A = +25^\circ\text{C}$		± 1	± 3	$^\circ\text{C}$
ADT 45		Over Rated Temperature		± 1.5	± 4	$^\circ\text{C}$
ADT 50		$T_A = +25^\circ\text{C}$		± 1	± 3	$^\circ\text{C}$
ADT 50		Over Rated Temperature		± 2	± 4	$^\circ\text{C}$
Scale Factor, ADT 45		$0^\circ\text{C} \leq T_A \leq +100^\circ\text{C}$	+9.8	+10	+10.2	$\text{mV}/^\circ\text{C}$
Scale Factor, ADT 50		$-40^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$	+9.8	+10	+10.2	$\text{mV}/^\circ\text{C}$
Load Regulation		$T_A = +25^\circ\text{C}$		5	20	$\text{m}^\circ\text{C}/\mu\text{A}$
		$0\ \mu\text{A} \leq I_L \leq 50\ \mu\text{A}$		20	100	$\text{m}^\circ\text{C}/\mu\text{A}$
Power Supply Rejection Ratio	PSRR	$T_A = +25^\circ\text{C}$		30	100	$\text{m}^\circ\text{C}/\text{V}$
		$3.0\text{ V} \leq +V_S \leq 5.5\text{ V}$		50	150	$\text{m}^\circ\text{C}/\text{V}$
Linearity				0.5		$^\circ\text{C}$
Long-Term Stability		$T_A = +150^\circ\text{C}$ for 1 khrs		0.4		$^\circ\text{C}$
OUTPUT						
ADT 45 Output Voltage		$T_A = +25^\circ\text{C}$		250		mV
ADT 50 Output Voltage		$T_A = +25^\circ\text{C}$		750		mV
Output Voltage Range			100		2,000	mV
Output Load Current	I_L		0		100	μA
Short-Circuit Current	I_{SC}	Note 1			250	μA
Capacitive Load Driving	C_L	No Oscillations ¹	1,000	10,000		pF
Device Turn-On Time		Output Within $\pm 1^\circ\text{C}$		0.5	1	ms
		100 k Ω 100 pF Load ¹				
POWER SUPPLY						
Supply Range	V_S		2.7		12	V
Supply Current	I_{SY}	Unloaded			60	μA

NOTES

¹Guaranteed but not tested.

Specifications subject to change without notice.

ABSOLUTE MAXIMUM RATINGS*

Supply Voltage	+13.2 V
Output Pin	$\text{GND} \leq V_{OUT} \leq +V_S$
Operating Temperature Range	-55°C to $+150^\circ\text{C}$
Die Junction Temperature	$+175^\circ\text{C}$
Storage Temperature Range	-65°C to $+160^\circ\text{C}$
Lead Temperature (Soldering, 60 sec)	$+300^\circ\text{C}$

*CAUTION

- Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation at or above this specification is not implied. Exposure to the above maximum rating conditions for extended periods may affect device reliability.
- Permanent damage may occur on unprotected units from high energy electrostatic fields. Keep units in conductive foam or packaging at all times until ready to use. Use proper antistatic handling procedures.
- Remove power before inserting or removing units from their sockets.

CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although the ADT 45/ADT 50 features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.

Package Type	θ_{JA}	θ_{JC}	Units
SOT-23 (RT Suffix)	300 ¹	180	$^\circ\text{C}/\text{W}$

NOTE

¹ θ_{JA} is specified for device in socket (worst case conditions).

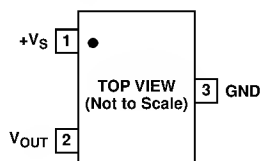


ORDERING GUIDE

Model	Accuracy at +25°C (°C max)	Linear Operating Temperature Range	Package Option*
ADT45GRT	±3.0	0°C to +100°C	RT-3
ADT50GRT	±3.0	-40°C to +125°C	RT-3

*RT = Surface Mount.

PIN CONFIGURATION



FUNCTIONAL DESCRIPTION

An equivalent circuit for the ADT45/ADT50 of micropower centigrade temperature sensors is shown in Figure 1. At the heart of the temperature sensor is a bandgap core comprised of transistors Q1 and Q2, biased by Q3 to approximately 8 μA. The bandgap core operates both Q1 and Q2 at the same collector current level; however, since the emitter area of Q1 is 10 times that of Q2, Q1's V_{BE} and Q2's V_{BE} are not equal by the following relationship:

$$\Delta V_{BE} = V_T \times \ln \left(\frac{A_{E,Q1}}{A_{E,Q2}} \right)$$

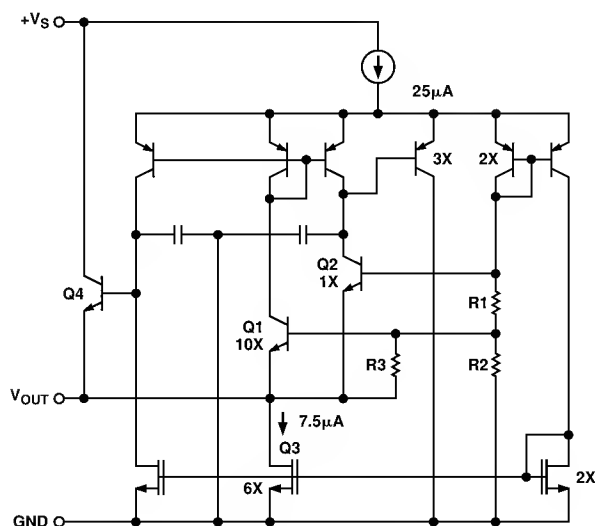


Figure 1. Temperature Sensor Simplified Equivalent Circuit

Resistors R1 and R2 are used to scale this result to produce the output voltage transfer characteristic of each temperature sensor and, simultaneously, R2 and R3 are used to scale Q1's V_{BE} as an offset term in V_{OUT} . Table I summarizes the differences between the two temperature sensors' output characteristics:

Table I. ADT45/ADT50 Output Characteristics

Sensor	Offset Voltage (V)	Output Voltage Scaling (mV/°C)	Output Voltage @ +25°C
ADT45	0	10	250 mV
ADT50	0.5	10	750 mV

The output voltage of the temperature sensor is available at the emitter of Q4, which buffers the bandgap core and provides load current drive. Q4's current gain, working with the available base current drive from the previous stage, sets the short-circuit current limit of these devices to 250 μA.

ADT45/ADT50- Typical Performance Characteristics

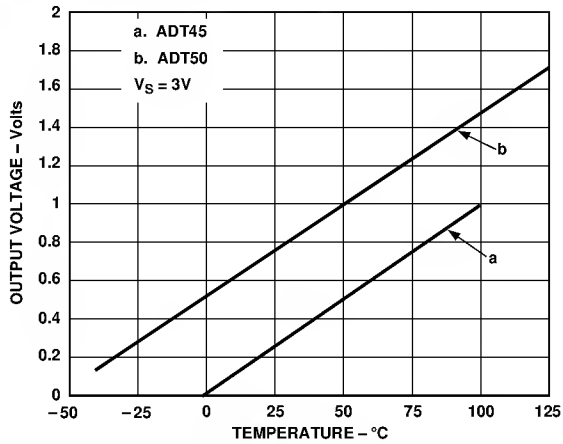


Figure 2. Output Voltage vs. Temperature

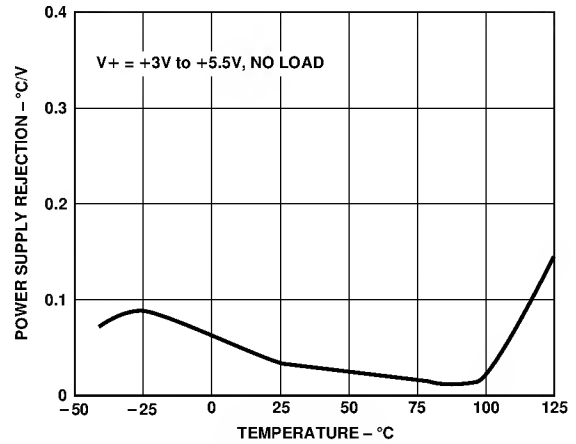


Figure 5. Power Supply Rejection vs. Temperature

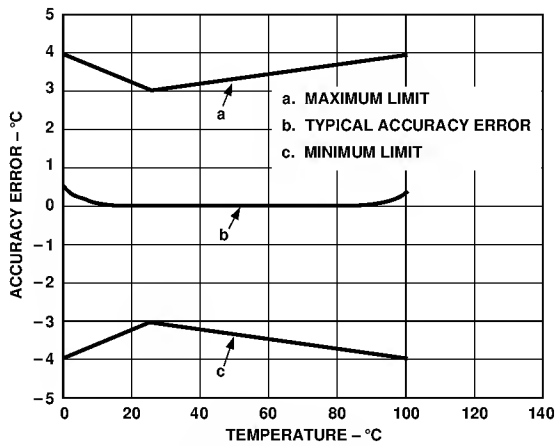


Figure 3. ADT45 Accuracy Error vs. Temperature

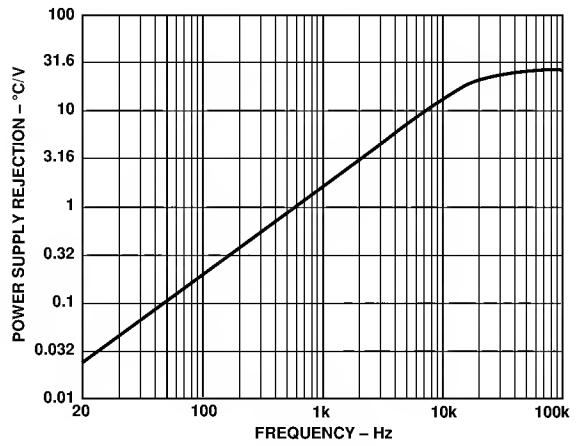


Figure 6. Power Supply Rejection vs. Frequency

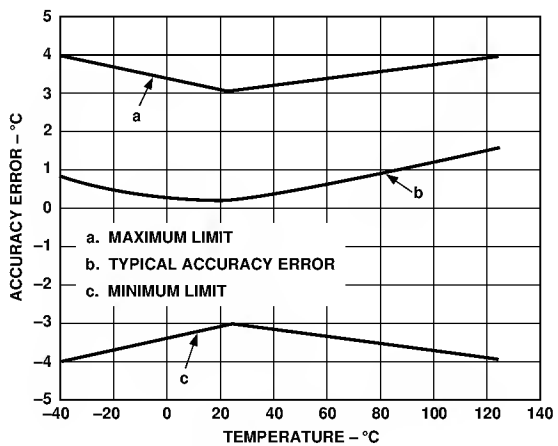


Figure 4. ADT50 Accuracy Error vs. Temperature

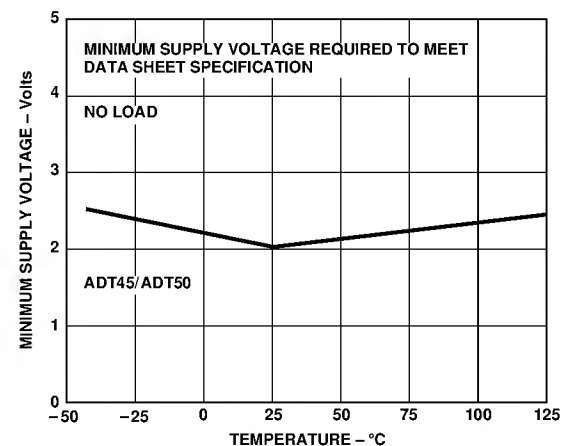


Figure 7. Minimum Supply Voltage vs. Temperature

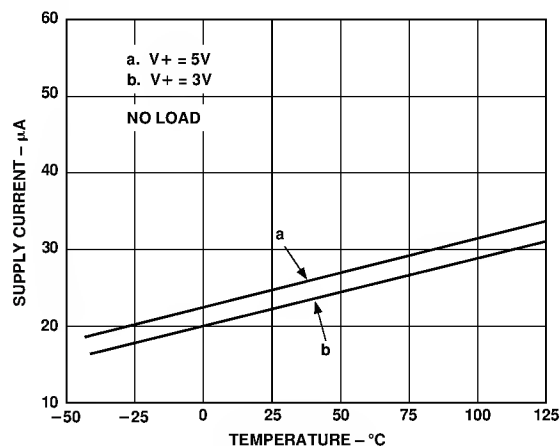


Figure 8. Supply Current vs. Temperature

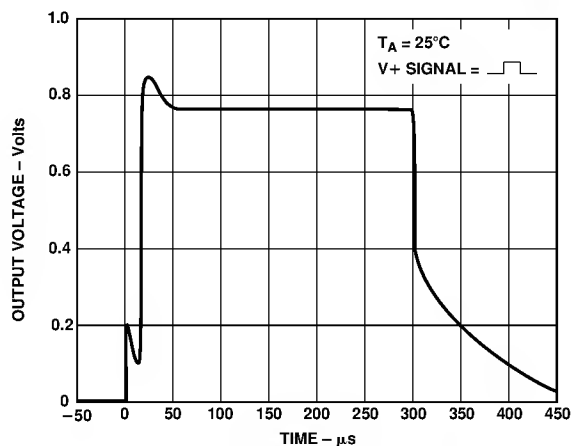


Figure 11. V_{OUT} Response Time to Power-On vs. Time

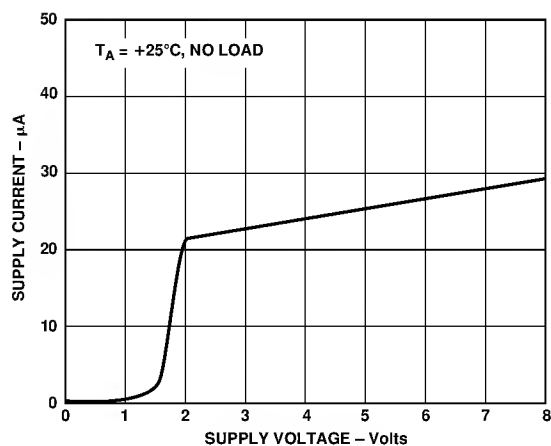


Figure 9. Supply Current vs. Supply Voltage

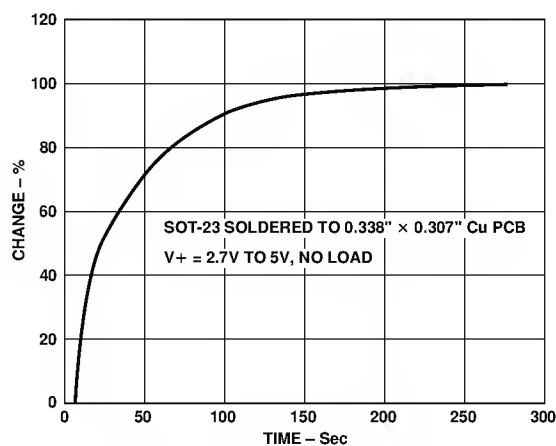


Figure 12. Thermal Response Time in Still Air

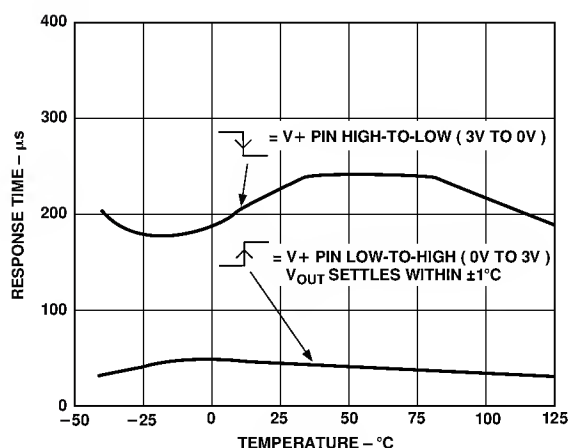


Figure 10. V_{OUT} Response Time for $V+$ Power-Up/Power-Down vs. Temperature

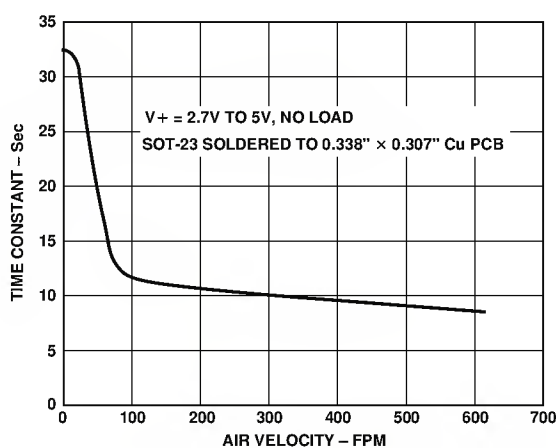


Figure 13. Thermal Response Time in Forced Air

ADT45/ADT50

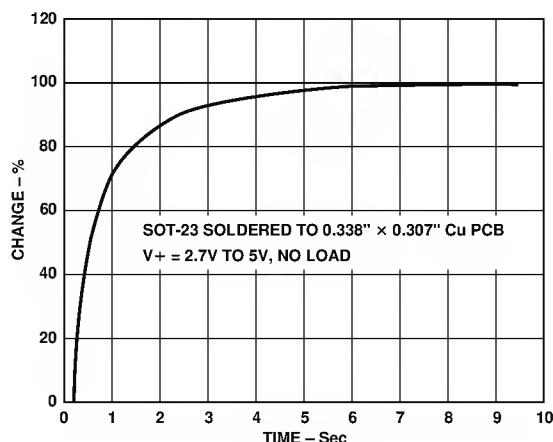


Figure 14. Thermal Response Time in Stirred Oil Bath

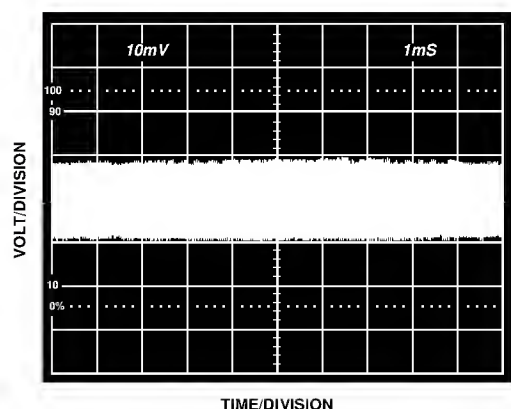


Figure 15. Temperature Sensor Wideband Output Noise Voltage. Gain = 100, BW = 157 kHz

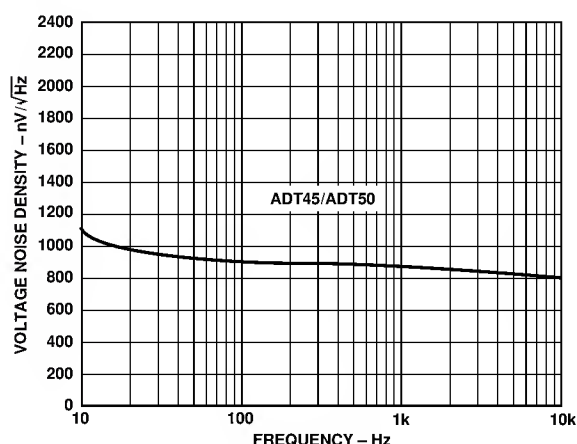


Figure 16. Voltage Noise Spectral Density vs. Frequency

APPLICATIONS SECTION

Mounting Considerations

If the ADT 45/ADT 50 temperature sensors are thermally attached and protected, they can be used in any temperature measurement application where the maximum temperature range of the medium is between -40°C to $+125^{\circ}\text{C}$. Properly cemented or glued to the surface of the medium, these sensors will be within 0.01°C of the surface temperature. Caution should be exercised as any wiring to the device can act as heat pipes, introducing errors if the surrounding air-surface interface is not isothermal. Avoiding this condition is easily achieved by dabbing the leads of the temperature sensor and the hookup wires with a bead of thermally conductive epoxy. This will ensure that the ADT 45/ADT 50 die temperature is not affected by the surrounding air temperature.

These temperature sensors, as well as any associated circuitry, should be kept insulated and dry to avoid leakage and corrosion. In wet or corrosive environments, any electrically isolated metal or ceramic well can be used to shield the temperature sensors. Condensation at very cold temperatures can cause errors and should be avoided by sealing the device using electrically non-conductive epoxy paints or dips, or any one of many printed circuit board coatings and varnishes.

Thermal Environment Effects

The thermal environment in which the ADT 45/ADT 50 sensors are used determines two important characteristics: self-heating effects and thermal response time. Illustrated in Figure 17 is a thermal model of the ADT 45/ADT 50 temperature sensors, which is useful in understanding these characteristics.

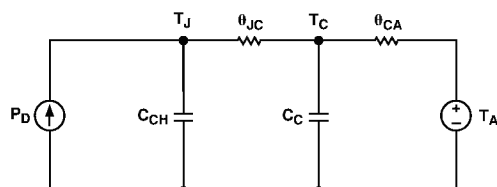


Figure 17. ADT45/ADT50 Thermal Circuit Model

In the SOT -23 package, the thermal resistance junction-to-case, θ_{JC} , is 180°C/W . The thermal resistance case-to-ambient, θ_{CA} , is the difference between θ_{JA} and θ_{JC} , and is determined by the characteristics of the thermal connection. The temperature sensor's power dissipation, represented by P_D , is the product of the total voltage across the device and its total supply current (including any current delivered to the load). The rise in die temperature above the medium's ambient temperature is given by:

$$T_J = P_D \times (\theta_{JC} + \theta_{CA}) + T_A$$

Thus, the die temperature rise of an ADT 45 "RT" package mounted into a socket in still air at 25°C and driven from a +5 V supply is less than 0.04°C .

The transient response of the ADT 45/ADT 50 sensors to a step change in the temperature is determined by the thermal resistances and the thermal capacities of the die, C_{CH} , and the case, C_C . The thermal capacity of the case, C_C , varies with the measurement medium since it includes anything that is in direct contact with the package. In all practical cases, the thermal capacity of the case is the limiting factor in the thermal response time of the sensor and can be represented by a single-pole RC-time constant response.

Figures 11 to 13 illustrate the thermal response time of the ADT 45/ADT 50 sensors under various conditions. The thermal time constant of a temperature sensor is defined to be the time required for the sensor to reach 63.2% of the final value for a step change in the temperature.

Basic Temperature Sensor Connections

The circuit in Figure 18 illustrates the basic circuit configuration for the ADT 45/ADT 50 temperature sensors.

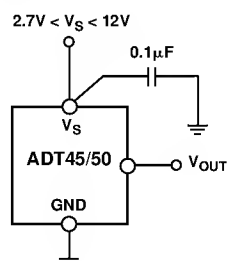


Figure 18. Basic Temperature Sensor Circuit Configuration

Note the 0.1 μF bypass capacitor on the input. This capacitor should be a ceramic type, have very short leads (surface mount would be preferable), and located as close a physical proximity to the temperature sensor supply pin as practical. Since these temperature sensors operate on very little supply current and could be exposed to very hostile electrical environments, it is important to minimize the effects of RFI (Radio-Frequency Interference) on these devices. The effect of RFI on these temperature sensors in specific and analog ICs in general is manifested as abnormal dc shifts in the output voltage due to the rectification of the high frequency ambient noise by the IC. In those cases where the devices are operated in the presence of high frequency radiated or conducted noise, a large value tantalum capacitor (>2.2 μF) placed across the 0.1 μF ceramic may offer additional noise immunity.

Fahrenheit Thermometers

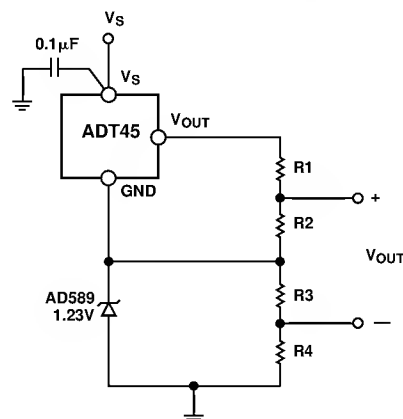
Although the ADT 45/ADT 50 temperature sensors are centigrade temperature sensors, a few components can be used to convert the output voltage and transfer characteristics to read Fahrenheit temperatures directly. Shown in Figure 19a is an example of a simple Fahrenheit thermometer using the ADT 45. This circuit can be used to sense temperatures from 41°F to 257°F with an output transfer characteristic of 1 mV/°F using the ADT 45. This particular approach does not lend itself well to the ADT 50 because of its inherent 0.5 V output offset. The circuit is constructed with an AD589, a 1.23 V voltage reference, and four resistors whose values are shown in the figure table. The scaling of the output resistance levels was to ensure minimum output loading on the temperature sensors. A generalized expression for the circuit's transfer equation is given by:

$$V_{OUT} = \left(\frac{R1}{R1 + R2} \right) (ADT\ 45) + \left(\frac{R3}{R3 + R4} \right) (AD589)$$

where ADT 45 = Output voltage of the ADT 45 at the measurement temperature, T_M , and

AD589 = Output voltage of the reference = 1.23 V.

Note that the output voltage of this circuit is not referenced to the circuit's common. If this output voltage were to be directly applied to the input of an ADC, the ADC's common should be adjusted accordingly.



SENSOR	TCV _{OUT}	R1 (kΩ)	R2 (kΩ)	R3 (kΩ)	R4 (kΩ)
ADT45	1mV/°F	45.3	10	10	374

Figure 19a. ADT45 Fahrenheit Thermometers

The same circuit principles can be applied to the ADT 50, but because of the ADT 50's inherent offset, the circuit uses two fewer resistors as shown in Figure 19b. In this circuit, the output voltage transfer characteristic is 1 mV/°F, but is referenced to the circuit's common; however, there is a 58 mV (58°F) offset in the output voltage. For example, the output voltage of the circuit would read 18 mV, if the ADT 50 is placed in -40°F ambient environment, and 315 mV at 257°F.

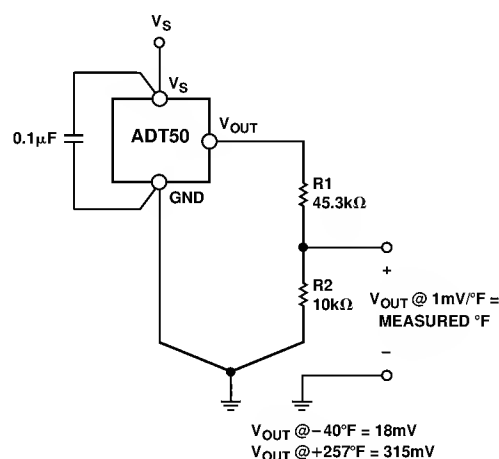


Figure 19b. ADT50 Fahrenheit Thermometer Version 1

At the expense of additional circuitry, the offset produced by the circuit in Figure 19b can be avoided by using the circuit in Figure 19c. In this circuit, the output of the ADT 50 is conditioned by a single-supply, micropower op amp, the OP193. Although the entire circuit operates from a single +3 V supply, the output voltage of the circuit reads the temperature directly with a transfer characteristic of 1 mV/°F, without offset. This is accomplished through the use of an ADM 660, a supply voltage inverter. The +3 V supply is inverted and applied to the OP193's V- terminal. Thus, for a temperature range between -40°F and +257°F, the output of the circuit reads -40 mV to +257 mV. A general expression for the circuit's transfer equation is given by:

$$V_{OUT} = \left(\frac{R6}{R5 + R6} \right) \left(1 + \frac{R4}{R3} \right) (ADT\ 50) - \left(\frac{R4}{R3} \right) \left(\frac{V_S}{2} \right)$$

ADT45/ADT50

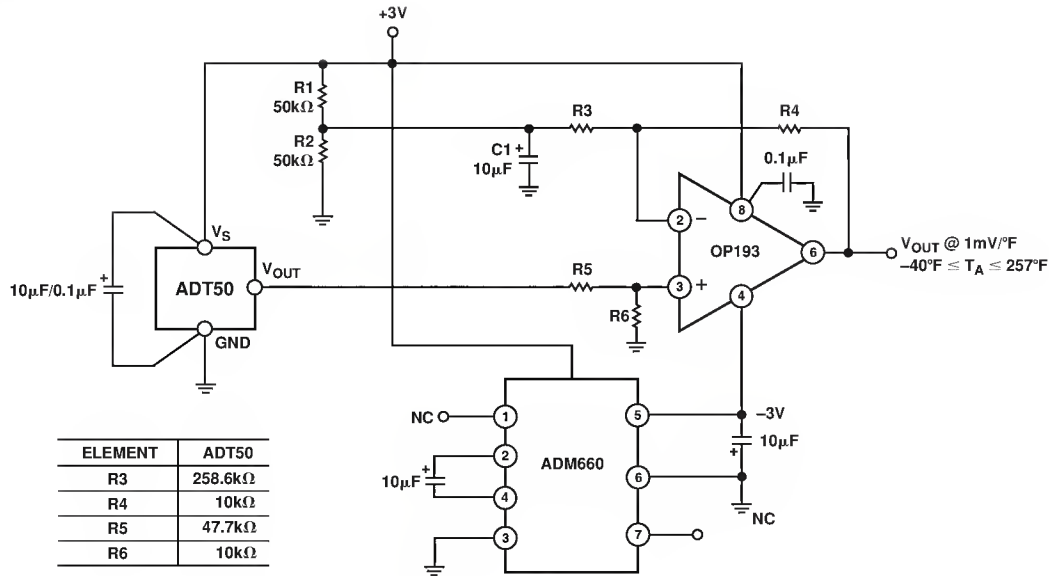


Figure 19c. ADT50 Fahrenheit Thermometer Version 2

Average and Differential Temperature Measurement

In many commercial and industrial environments, temperature sensors are often used to measure the average temperature in a building, or the difference in temperature between two locations on a factory floor or in an industrial process. The circuits in Figures 20a and 20b demonstrate an inexpensive approach to average and differential temperature measurement. In Figure 20a, an OP193 is used to amplify the stepped down averaged output of three temperature sensors to produce an output voltage scaled to 10 mV/°C, which represents the average temperature at three locations. The circuit can be extended to as many temperature sensors as required as long as the circuit's transfer equation is maintained. In this application, it is recommended that one temperature sensor type be used throughout the circuit; otherwise, the output voltage of the circuit will not produce an accurate reading of the various ambient conditions.

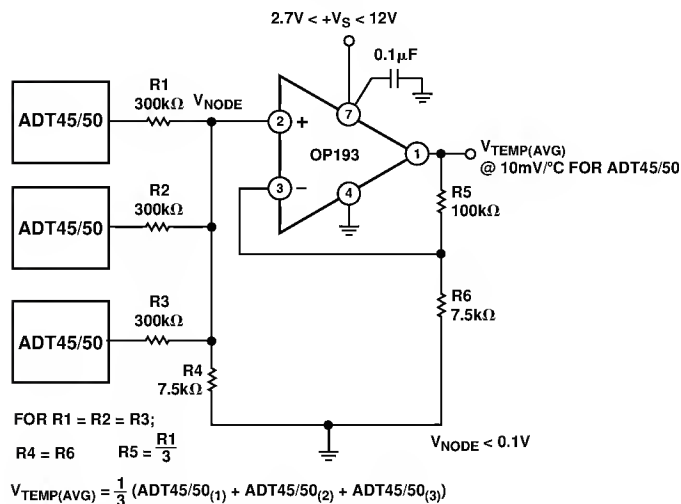


Figure 20a. Configuring Multiple Sensors for Average Temperature Measurements

The circuit in Figure 20b illustrates how a pair of ADT 50 sensors can be used with an OP193 configured as a difference amplifier to read the difference in temperature between two locations. In these

applications, it is always possible that one temperature sensor would be reading a temperature below that of the other sensor. To accommodate this condition, the output of the OP193 is offset to a voltage at one-half the supply via R5 and R6. Thus, the output voltage of the circuit is measured relative to this point, as shown in the figure. Using the ADT 50, the output voltage of the circuit is scaled by 10 mV/°C. To minimize error in the difference between the two measured temperatures, a common, readily available thin-film resistor network is used for R1-R4.

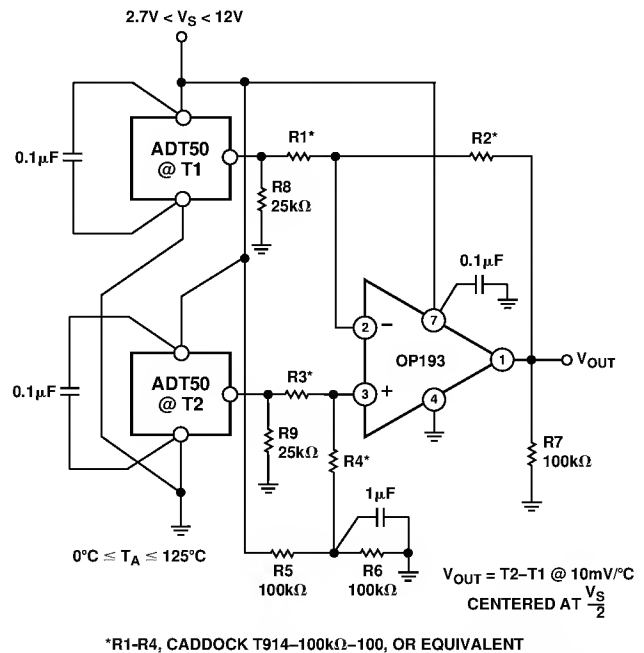


Figure 20b. Configuring Multiple Sensors for Differential Temperature Measurements

Microprocessor Interrupt Generator

These inexpensive temperature sensors can be used with a voltage reference and an analog comparator to configure an interrupt generator useful in microprocessor applications. With the popularity of fast 486 and Pentium laptop computers, the need

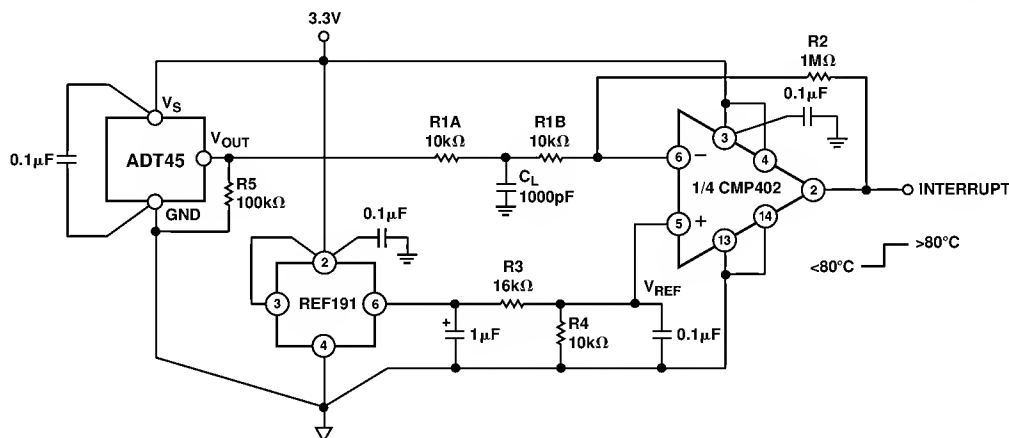


Figure 21. Pentium™ Over Temperature Interrupt Generator

to indicate that a microprocessor over-temperature condition has grown tremendously. The circuit illustrated in Figure 21 demonstrates one way to generate an interrupt using an ADT45, a CMP402 analog comparator, and a REF191, a 2 V precision voltage reference.

The circuit has been designed to produce a logic HIGH interrupt signal if the microprocessor temperature exceeds +80°C. This 80°C trip point was chosen arbitrarily (final value set by the microprocessor thermal reference design) and is set using an R3-R4 voltage divider of the REF191's output voltage. Since the output of the ADT45 is scaled by 10 mV/°C, the voltage at the CMP402's inverting terminal is set to 0.8 V.

Since temperature is a slowly moving quantity, the possibility for comparator chatter exists. To avoid this condition, hysteresis is used around the comparator. In this application, a hysteresis of 5°C about the trip point was arbitrarily chosen; the ultimate value for hysteresis should be determined by the end application. The output logic voltage swing of the comparator with R1 and R2 determine the amount of comparator hysteresis. Using a 3.3 V supply, the output logic voltage swing of the CMP402 is 2.6 V; thus, for a hysteresis of +5°C (50 mV @ 10 mV/°C), R1 is set to 20 kΩ and R2 is set to 1 MΩ. An expression for this circuit's hysteresis is given by:

$$V_{HYS} = \left(\frac{R1}{R2} \right) (V_{LOGIC SWING, CMP02})$$

Since the likelihood that this circuit would be used in close proximity to high speed digital circuits, R1 is split into equal values and a 1000 pF is used to form a low-pass filter on the output of the ADT45. Furthermore, to prevent high frequency noise from contaminating the comparator trip point, a 0.1 μF capacitor is used across R4.

Thermocouple Signal Conditioning with Cold-Junction Compensation

The circuit in Figure 22 conditions the output of a Type K thermocouple, while providing cold-junction compensation, for temperatures between 0°C and 250°C. The circuit operates from single +3.3 V to +12 V supplies and has been designed to produce an output voltage transfer characteristic of 10 mV/°C.

A Type K thermocouple exhibits a Seebeck coefficient of approximately 41 μV/°C; therefore, at the cold junction, the ADT45 with a temperature coefficient of 10 mV/°C is used with R1 and R2 to introduce an opposing cold-junction temperature coefficient of -41 μV/°C. This prevents the isothermal, cold-junction connection between the circuit's PCB tracks and the thermocouple's wires from introducing an error in the measured temperature. This compensation works extremely well for

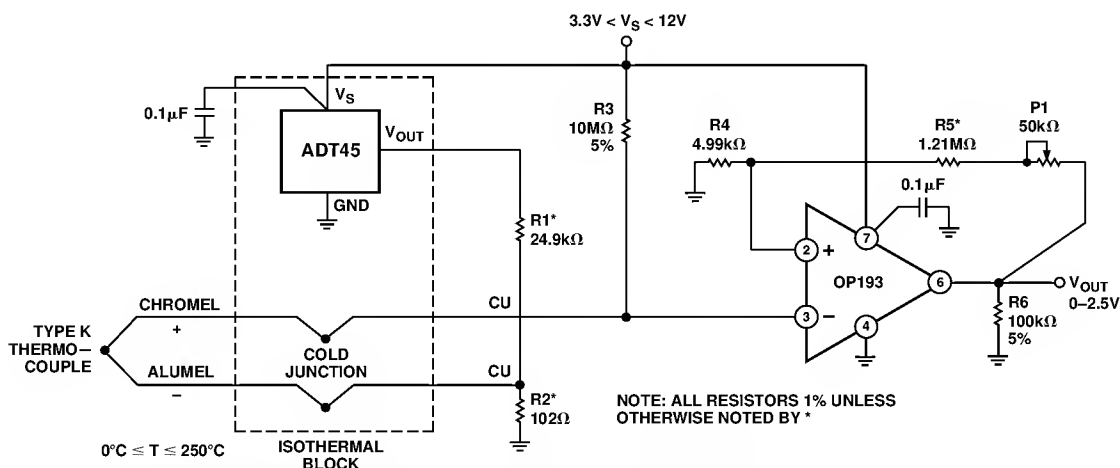


Figure 22. A Single Supply, Type K Thermocouple Signal Condition Circuit with Cold-Junction Compensation

ADT45/ADT50

circuit ambient temperatures in the range of +20°C to +50°C. Over a 250°C measurement temperature range, the thermocouple produces an output voltage change of 10.151 mV. Since the required circuit's output full-scale voltage is 2.5 V, the gain of the circuit is set to 246.3. Choosing R4 equal to 4.99 kΩ sets R5 equal to 1.22 MΩ. Since the closest 1% value for R5 is 1.21 MΩ, a 50 kΩ potentiometer is used with R5 for fine trim of the full-scale output voltage. Although the OP193 is a superior single-supply, micropower operational amplifier, its output stage is not rail-to-rail; as such, the 0°C output voltage level is 0.1 V. If this circuit were to be digitized by a single-supply ADC, the ADC's common should be adjusted accordingly to 0.1 V.

Using ADT45/ADT50 Sensors in Remote Locations

In many industrial environments, sensors are required to operate in the presence of high ambient noise. These noise sources take on many forms; for example, SCR transients, relays, radio transmitters, arc welders, ac motors, et cetera. Furthermore, they may be used at considerable distance from the signal conditioning circuitry. These high noise environments are very typically in the form of electric fields, so the voltage output of the temperature sensor can be susceptible to contamination from these noise sources.

Illustrated in Figure 23 is a way to convert the output voltage of an ADT45/ADT50 sensor into a current to be transmitted down a long twisted-pair shielded cable to a ground referenced receiver. The temperature sensors do not possess the capability of high output current operation; thus, a garden variety PNP transistor is used to boost the output current drive of the circuit. As shown in the table, the values of R2 and R3 were chosen to produce an arbitrary full-scale output current of 2 mA. Lower values for the full-scale current are not recommended because the minimum-scale output current produced by the circuit could be contaminated by nearby ambient magnetic fields operating in the vicinity of the circuit/cable pair. Because of the use of an external transistor, the minimum recommended operating voltage for this circuit is +5 V. Note, to minimize the effects of EMI (or RFI) both the circuit's and the temperature sensor's supply pins are bypassed with good quality, ceramic capacitors.

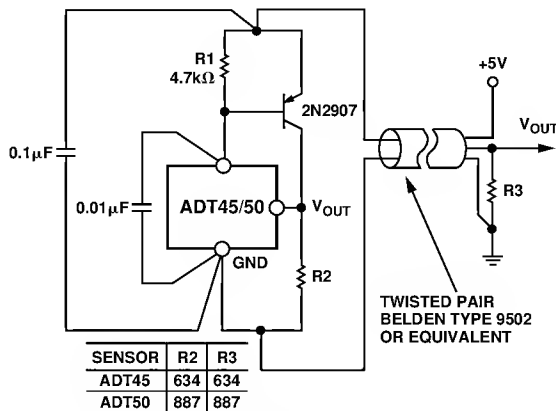


Figure 23. A Remote, Two-Wire Boosted Output Current Temperature Sensor

A Temperature to 4 mA–20 mA Loop Transmitter

In many process control applications, two-wire transmitters are used to convey analog signals through noisy ambient environments. These current transmitters use a “zero-scale” signal current of 4 mA that can be used to power the transmitter's

signal conditioning circuitry. The “full-scale” output signal in these transmitters is 20 mA.

A circuit that transmits temperature information in this fashion is illustrated in Figure 25. Using an ADT45/ADT50 as the temperature sensor, the output current is linearly proportional to the temperature of the medium. The entire circuit operates from the REF193's +3 V output. The REF193 requires no external trimming for two reasons: (1) the REF193's tight initial output voltage tolerance and (2) the low supply current of the ADT45/ADT50, the OP193 and the REF193. The entire circuit consumes less than 3 mA from a total budget of 4 mA. The OP193 regulates the output current to satisfy the current summation at the noninverting node of the OP193. A generalized expression for the KCL equation at the OP193's Pin 3 is given by:

$$I_{OUT} = \frac{1}{R7} \times \left(\frac{ADT45/ADT50 \times R3}{R1} + \frac{V_{REF} \times R3}{R2} \right)$$

For each of the three temperature sensors, Table II provides the values for each of the components, P1, P2 and R1–R4:

Table II. Circuit Element Values for Loop Transmitter

Sensor	R1(Ω)	P1(Ω)	R2(Ω)	P2(Ω)	R3(Ω)	R4(Ω)
ADT45	97.6 k	5 k	1.58 M	100 k	140 k	56.2 k
ADT50	97.6 k	5 k	931 k	50 k	97.6 k	47 k

The 4 mA offset trim is provided by P2, and P1 provides the circuit's full-scale gain trim at 20 mA. These two trims do not interact because the noninverting input of the OP193 is held at a virtual ground. The zero-scale and full-scale output currents of the circuit are adjusted according to the operating temperature range of each temperature sensor. The Schottky diode, D1, is required in this circuit to prevent loop supply power-on transients from pulling the noninverting input of the OP193 more than 300 mV below its inverting input. Without this diode, such transients could cause phase reversal of the operational amplifier and possible latchup of the transmitter. The loop supply voltage compliance of the circuit is limited by the maximum applied input voltage to the REF193 and is from +9 V to +18 V.

A Temperature-to-Frequency Converter

Another common method of transmitting analog information from a remote location is to convert a voltage to an equivalent in the frequency domain. This is readily done with any of the low cost, monolithic voltage-to-frequency converters (VFCs) available. These VFCs feature a robust, open-collector output transistor for easy interfacing to digital circuitry. The digital signal produced by the VFC is less susceptible to contamination from external noise sources and line voltage drops because the only important information is the frequency of the digital signal. As long as the conversions between temperature and frequency are done accurately, the temperature data from the sensors can be reliably transmitted.

The circuit in Figure 24 illustrates a method by which the outputs of these temperature sensors can be converted to a frequency using the AD654. The output signal of the AD654 is a square wave that is proportional to the dc input voltage across Pins 4 and 3. The transfer equation of the circuit is given by:

$$f_{OUT} = \frac{V_{TMP} - V_{OFFSET}}{10 \times (R_T \times C_T)}$$

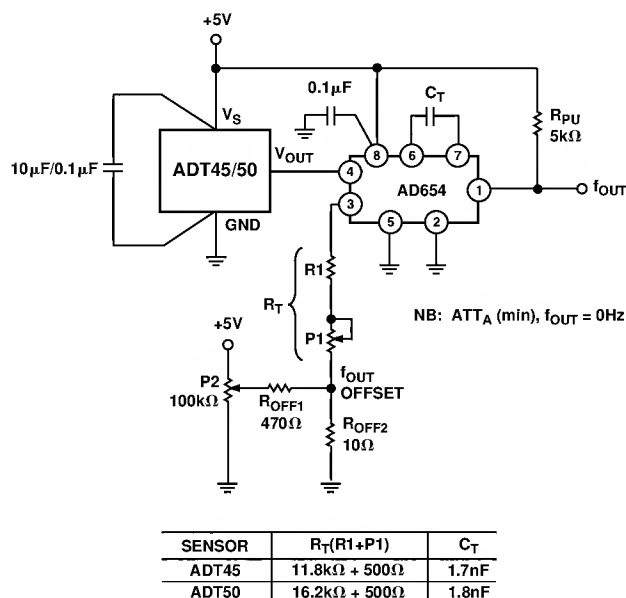


Figure 24. A Temperature-to-Frequency Converter

An offset trim network (f_{OUT} OFFSET) is included with this circuit to set f_{OUT} at 0 Hz when the temperature sensor's minimum output voltage is reached. Potentiometer P1 is required to calibrate the absolute accuracy of the AD 654. The table in the figure illustrates the circuit element values for each of the three sensors. The nominal offset voltage required for 0 Hz output from the ADT 45 is 50 mV; for the ADT 50, the offset voltage required is 100 mV. In all cases for the circuit values shown, the output frequency transfer characteristic of the circuit was set at 50 Hz/°C. At the receiving end, a frequency-to-voltage converter (FVC) can be used to convert the frequency back to a dc voltage for further processing. One such FVC is the AD 650.

For complete information on the AD 650 and AD 654, please consult the individual data sheets for those devices.

Driving Long Cables or Heavy Capacitive Loads

Although the ADT 45/ADT 50 temperature sensors are capable of driving capacitive loads up to 10,000 pF without oscillation, output voltage transient response times can be improved with the use of a small resistor in series with the output of the temperature sensor, as shown in Figure 26. As an added benefit, this resistor forms a low-pass filter with the cable's capacitance, which helps to reduce bandwidth noise. Furthermore, since the temperature sensor is likely to be used in environments where the ambient noise level can be very high, this resistor helps to prevent rectification by the devices of the high frequency noise. The combination of this resistor and the supply bypass capacitor offers the best protection.

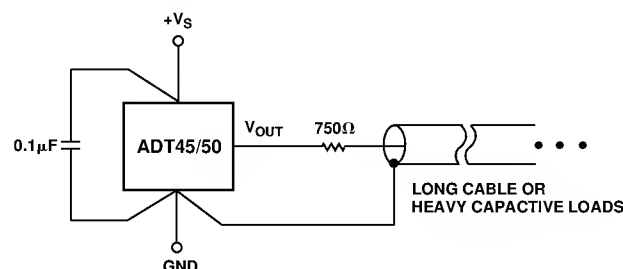


Figure 26. Driving Long Cables or Heavy Capacitive Loads

Commentary on Long-Term Stability

The concept of long-term stability has been used for many years to describe by what amount an IC's parameter would shift during its lifetime. This is a concept that has been typically applied to both voltage references and monolithic temperature sensors. Unfortunately, integrated circuits cannot be evaluated at room temperature (+25°C) for 10 years or so to determine this shift. As a result, manufacturers very typically perform accelerated lifetime testing of integrated circuits by operating ICs at elevated temperatures (between +125°C and +150°C) over a shorter period of time (typically, between 500 and 1000 hours).

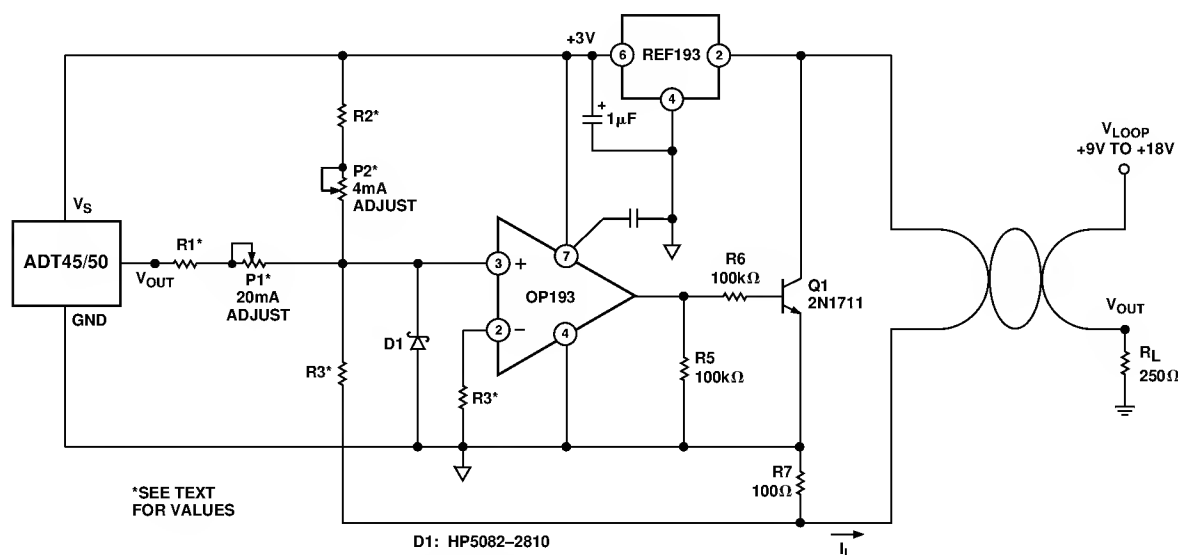


Figure 25. A Temperature to 4 mA-to-20 mA Loop Transmitter

C3175-8-10/97

For example, if the desired operating temperature of an IC is +25°C and has been subjected to test temperature of +150°C, the acceleration factor is:

With this background information, the ADT 45/ADT 50's long-term stability can be mapped to what its equivalent observed shift would be at $T_A = +25^{\circ}\text{C}$. As quoted in the data sheet, the long-term stability of these temperature sensors after 1000 hours at $+150^{\circ}\text{C}$ is 0.4°C . This shift is equivalent to $0.01^{\circ}\text{C}/\text{day}$ at $T_J = +150^{\circ}\text{C}$. To determine what the observed shift would be at $T_A = +25^{\circ}\text{C}$ is a matter of applying the acceleration factor calculated above to this result:

T hus, if any of the ADT 45/ADT 50 devices were to be used at 25°C , then the observed shift would be no more than 0.003 m°C per day, or 0.1 m°C per month. Calculating the observed shift for any other operating temperature is simply a matter of calculating a new acceleration factor.

where F = Calculated acceleration factor;

$$T_1 = +25^{\circ}\text{C} = 298.15\text{K}$$

Dimensions shown in inches and (mm).

The drawing illustrates the mechanical specifications of the 100-pin connector. It includes three views: a top view, a side view, and a detail view of the contact pins.

Top View Dimensions:

- Overall width: 0.1200 (3.048)
- Width between pins 1 and 2: 0.1102 (2.799)
- Pin 1 diameter: 0.0550 (1.397)
- Pin 2 diameter: 0.0470 (1.194)
- Pin 3 diameter: 0.1040 (2.642)
- Pin 4 diameter: 0.0827 (2.101)
- Pin 1 to Pin 2 distance: 0.0236 (0.599)
- Pin 2 to Pin 3 distance: 0.0177 (0.450)
- Pin 3 to Pin 4 distance: 0.0413 (1.049)
- Pin 4 to Pin 5 distance: 0.0374 (0.950)
- Pin 1 to Pin 3 distance: 0.0807 (2.050)
- Pin 3 to Pin 5 distance: 0.0701 (1.781)

Side View Dimensions:

- Overall height: 0.0440 (1.118)
- Height from seating plane to top of pin: 0.0320 (0.813)
- Seating plane to bottom of pin: 0.0040 (0.102)
- Bottom of pin to bottom of pin: 0.0005 (0.013)
- Pin 1 to Pin 2 distance: 0.0210 (0.533)
- Pin 2 to Pin 3 distance: 0.0146 (0.371)

Detail View Dimensions:

- Pin diameter: 0.0059 (0.150)
- Pin to pin distance: 0.0034 (0.086)
- Pin to pin distance: 0.0100 (0.254)
- Pin to pin distance: 0.0050 (0.127)
- Pin to pin distance: 0.027 (0.686)
- Pin to pin distance: REF